

Refine Search

Search Results -

| Terms | Documents |
|------------------------------------------------------------------------------------|-----------|
| (memory near2 controller) same (switch\$3 near5 path) same (enabl\$3 or disabl\$3) | 21 |

Database:

| |
|---------------------------------------------|
| US Pre-Grant Publication Full-Text Database |
| US Patents Full-Text Database |
| US OCR Full-Text Database |
| EPO Abstracts Database |
| JPO Abstracts Database |
| Derwent World Patents Index |
| IBM Technical Disclosure Bulletins |

Search:

Refine Search

Recall Text
Clear
Interrupt

Search History

DATE: Tuesday, April 27, 2004 [Printable Copy](#) [Create Case](#)

| <u>Set</u> | <u>Name</u> | <u>Query</u> | <u>Hit Count</u> | <u>Set Name</u> |
|--------------------------|-------------|---------------------------------------------------------------------------------------|------------------|-----------------|
| side by side | | | | result set |
| DB=USPT; PLUR=YES; OP=OR | | | | |
| <u>L3</u> | | (memory near2 controller) same (switch\$3 near5 path) same (enabl\$3 or disabl\$3) | 21 | <u>L3</u> |
| <u>L2</u> | | (memory near2 controller) same (switch\$3 near5 path) same enabl\$3 | 21 | <u>L2</u> |
| <u>L1</u> | | (memory near2 controller) same (switch\$3 near5 path) same enabl\$3 same disabl\$3 | 0 | <u>L1</u> |

END OF SEARCH HISTORY

Refine Search

Search Results -

| Terms | Documents |
|-------|-----------|
| L3 | 0 |

Database:

[US Pre-Grant Publication Full-Text Database](#)
[US Patents Full-Text Database](#)
[US OCR Full-Text Database](#)
[EPO Abstracts Database](#)
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| <u>Set</u> | <u>Name</u> | <u>Query</u> | <u>Hit Count</u> | <u>Set Name</u> |
|----------------------------------------------|-------------|---------------------------------------------------------------------------------------|------------------|-----------------|
| side by side | | | | result set |
| DB=USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=OR | L4 | L3 | 0 | <u>L4</u> |
| DB=USPT; PLUR=YES; OP=OR | L3 | (memory near2 controller) same (switch\$3 near5 path) same (enabl\$3 or disabl\$3) | 21 | <u>L3</u> |
| | L2 | (memory near2 controller) same (switch\$3 near5 path) same enabl\$3 | 21 | <u>L2</u> |
| | L1 | (memory near2 controller) same (switch\$3 near5 path) same enabl\$3 same disabl\$3 | 0 | <u>L1</u> |

END OF SEARCH HISTORY

Refine Search

Search Results -

| Terms | Documents |
|---------------------------------------------|-----------|
| L13 and (enabl\$3 same disabl\$3 same path) | 11 |

Database:

| |
|---------------------------------------------|
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Search:

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|-----|--------------------------------------------------|
| L15 | <input type="button" value="Refine Search"/> |
|-----|--------------------------------------------------|

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| <u>Set</u> | <u>Name</u> | <u>Query</u> | <u>Hit Count</u> | <u>Set Name</u> |
|---------------------------------|-----------------------------------------------------------------------------------------------------------|--------------|------------------|-----------------|
| side by side | | | | result set |
| <i>DB=USPT; PLUR=YES; OP=OR</i> | | | | |
| <u>L15</u> | L13 and (enabl\$3 same disabl\$3 same path) | | 11 | <u>L15</u> |
| <u>L14</u> | L13 and (enabl\$3 same disabl\$3) | | 90 | <u>L14</u> |
| <u>L13</u> | (710/316.ccls. or 710/317.ccls.) | | 541 | <u>L13</u> |
| <u>L12</u> | L6 and (710/316.ccls. or 710/317.ccls.) | | 3 | <u>L12</u> |
| <u>L11</u> | L6 and (710/316,317.ccls.) | | 3 | <u>L11</u> |
| <u>L10</u> | L9 same controller | | 4 | <u>L10</u> |
| <u>L9</u> | l6 same memory | | 61 | <u>L9</u> |
| <u>L8</u> | L6.ab. | | 22 | <u>L8</u> |
| <u>L7</u> | L6 same backplane | | 2 | <u>L7</u> |
| <u>L6</u> | (switch\$3 near5 path) same enabl\$3 same disabl\$3 | | 538 | <u>L6</u> |
| <u>L5</u> | (first adj1 memory adj1 controller) same (second adj1 memory adj1 controller) same (switch\$3 near5 path) | | 1 | <u>L5</u> |

DB=USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=OR

L4 L30 L4

DB=USPT; PLUR=YES; OP=OR

L3 (memory near2 controller) same (switch\$3 near5 path) same (enabl\$3 or
disabl\$3)21 L3L2 (memory near2 controller) same (switch\$3 near5 path) same enabl\$321 L2L1 (memory near2 controller) same (switch\$3 near5 path) same enabl\$3 same
disabl\$30 L1

END OF SEARCH HISTORY

Refine Search

Search Results -

| Terms | Documents |
|--------------------------------------------------------------------------------------------------------------|-----------|
| (first adj1 memory adj1 controller) same (second adj1 memory adj1 controller) same (switch\$3 near5 path) | 1 |

Database:

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Search:

Search History

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| <u>Set</u> <u>Name</u> <u>Query</u> | <u>Hit</u> <u>Count</u> | <u>Set</u> <u>Name</u> |
|---------------------------------------------------------------------------------------------------------------------|----------------------------|---------------------------|
| side by side | | |
| <i>DB=USPT; PLUR=YES; OP=OR</i> | | |
| <u>L5</u> (first adj1 memory adj1 controller) same (second adj1 memory adj1 controller) same (switch\$3 near5 path) | 1 | <u>L5</u> |
| <i>DB=USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=OR</i> | | |
| <u>L4</u> L3 | 0 | <u>L4</u> |
| <i>DB=USPT; PLUR=YES; OP=OR</i> | | |
| <u>L3</u> (memory near2 controller) same (switch\$3 near5 path) same (enabl\$3 or disabl\$3) | 21 | <u>L3</u> |
| <u>L2</u> (memory near2 controller) same (switch\$3 near5 path) same enabl\$3 | 21 | <u>L2</u> |
| <u>L1</u> (memory near2 controller) same (switch\$3 near5 path) same enabl\$3 same disabl\$3 | 0 | <u>L1</u> |

END OF SEARCH HISTORY

Refine Search

Search Results -

| Terms | Documents |
|-------------------|-----------|
| L6 same backplane | 2 |

Database:

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|---------------------------------------------|
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Search:

L7

Refine Search

Recall Text

Clear

Interrupt

Search History

DATE: Tuesday, April 27, 2004 [Printable Copy](#) [Create Case](#)

| <u>Set</u> | <u>Name</u> | <u>Query</u> | <u>Hit Count</u> | <u>Set Name</u> |
|-----------------------------------------------------|-----------------------------------------------------------------------------------------------------------|--------------|------------------|-----------------|
| side by side | | | | result set |
| <i>DB=USPT; PLUR=YES; OP=OR</i> | | | | |
| <u>L7</u> | L6 same backplane | | 2 | <u>L7</u> |
| <u>L6</u> | (switch\$3 near5 path) same enabl\$3 same disabl\$3 | | 538 | <u>L6</u> |
| <u>L5</u> | (first adj1 memory adj1 controller) same (second adj1 memory adj1 controller) same (switch\$3 near5 path) | | 1 | <u>L5</u> |
| <i>DB=USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=OR</i> | | | | |
| <u>L4</u> | L3 | | 0 | <u>L4</u> |
| <i>DB=USPT; PLUR=YES; OP=OR</i> | | | | |
| <u>L3</u> | (memory near2 controller) same (switch\$3 near5 path) same (enabl\$3 or disabl\$3) | | 21 | <u>L3</u> |
| <u>L2</u> | (memory near2 controller) same (switch\$3 near5 path) same enabl\$3 | | 21 | <u>L2</u> |
| <u>L1</u> | (memory near2 controller) same (switch\$3 near5 path) same enabl\$3 same disabl\$3 | | 0 | <u>L1</u> |

Refine Search

Search Results -

| Terms | Documents |
|--------|-----------|
| L6.ab. | 22 |

Database:

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|---------------------------------------------|
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Search:

| | |
|--------------------------------------------------------------------------------------------------------------------------|---------------------------------------------------------------------------------------|
| L8 | <input type="checkbox"/> <input checked="" type="checkbox"/> <input type="checkbox"/> |
| <input type="button" value="Refine Search"/> | |
| <input type="button" value="Recall Text"/> <input type="button" value="Clear"/> <input type="button" value="Interrupt"/> | |

Search History

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| <u>Set</u> | <u>Name</u> | <u>Query</u> | <u>Hit Count</u> | <u>Set Name</u> |
|-----------------------------------------------------|-----------------------------------------------------------------------------------------------------------|--------------|------------------|-----------------|
| side by side | | | | result set |
| <i>DB=USPT; PLUR=YES; OP=OR</i> | | | | |
| <u>L8</u> | L6.ab. | | 22 | <u>L8</u> |
| <u>L7</u> | L6 same backplane | | 2 | <u>L7</u> |
| <u>L6</u> | (switch\$3 near5 path) same enabl\$3 same disabl\$3 | | 538 | <u>L6</u> |
| <u>L5</u> | (first adj1 memory adj1 controller) same (second adj1 memory adj1 controller) same (switch\$3 near5 path) | | 1 | <u>L5</u> |
| <i>DB=USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=OR</i> | | | | |
| <u>L4</u> | L3 | | 0 | <u>L4</u> |
| <i>DB=USPT; PLUR=YES; OP=OR</i> | | | | |
| <u>L3</u> | (memory near2 controller) same (switch\$3 near5 path) same (enabl\$3 or disabl\$3) | | 21 | <u>L3</u> |
| <u>L2</u> | (memory near2 controller) same (switch\$3 near5 path) same enabl\$3 | | 21 | <u>L2</u> |
| <u>L1</u> | (memory near2 controller) same (switch\$3 near5 path) same enabl\$3 same disabl\$3 | | 0 | <u>L1</u> |